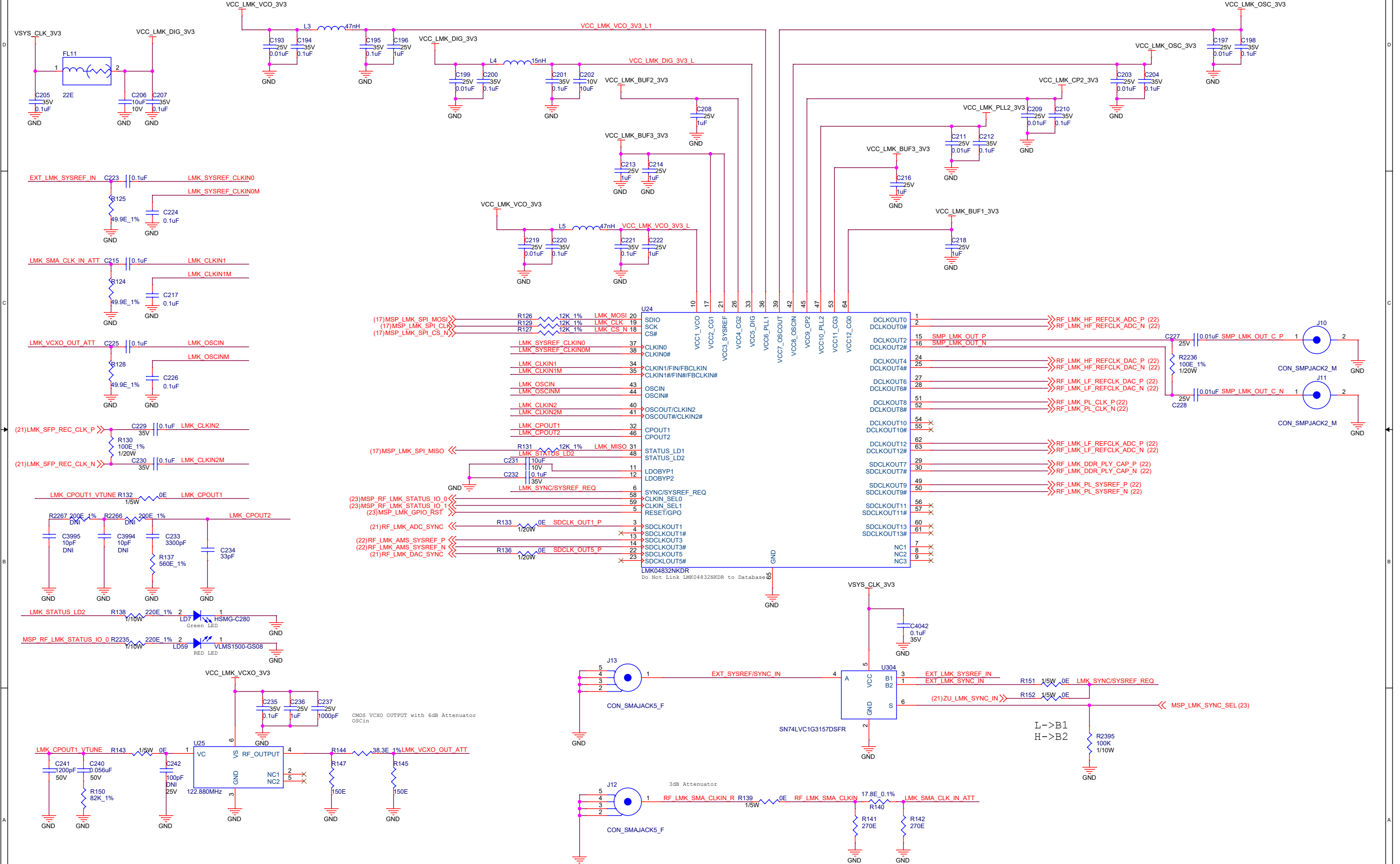


RF DATA CONVERTER REFERENCE CLOCK GENERATOR



Title			
<Title>			
Size C	Document Number		Rev
	<Doc>		<Rev Code>
Date:	Monday, January 11, 2021	Sheet 1 of 1	

D

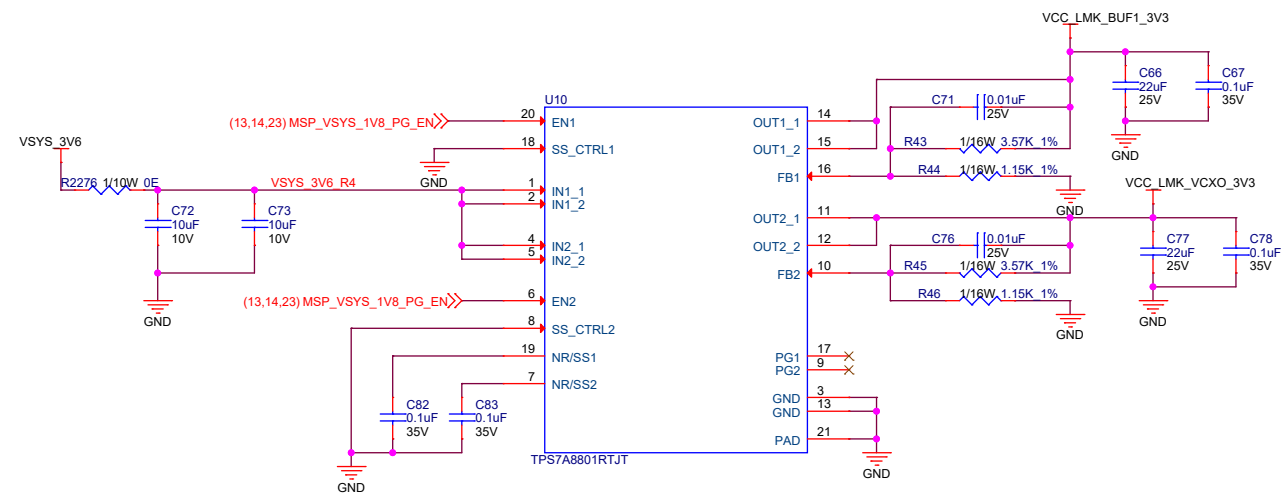
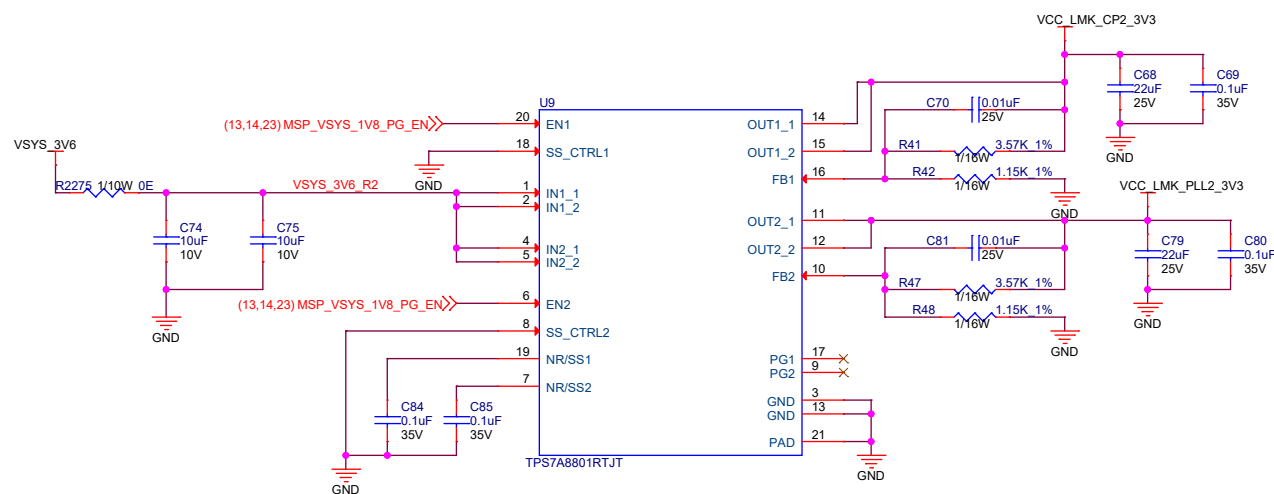
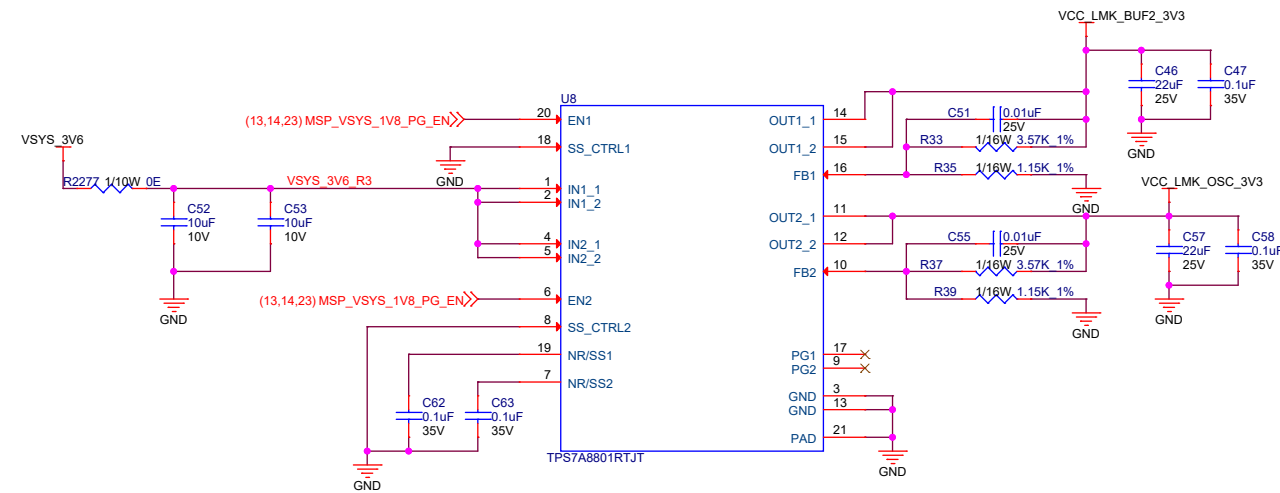
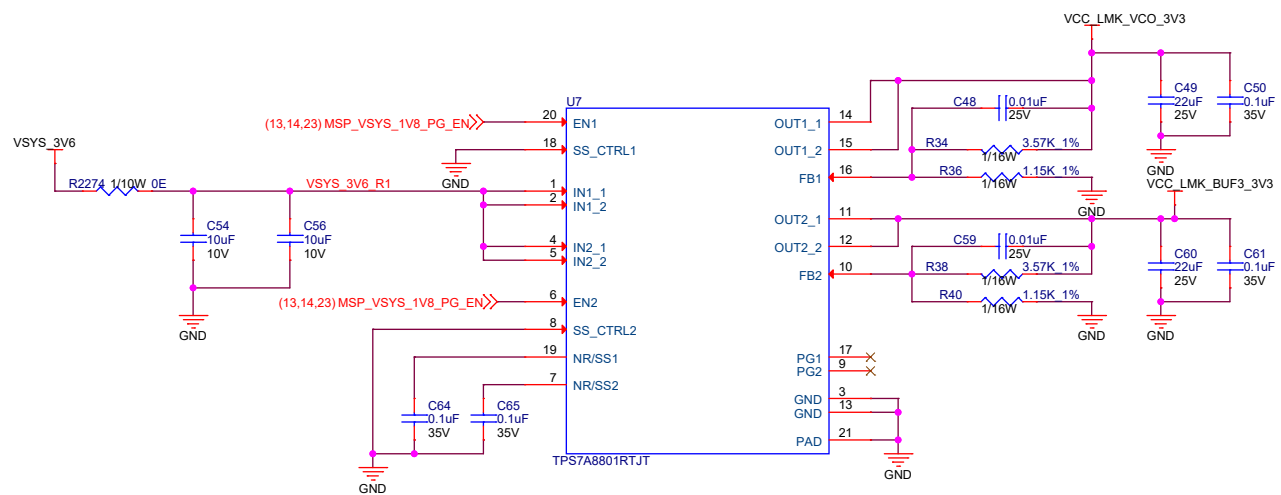


B

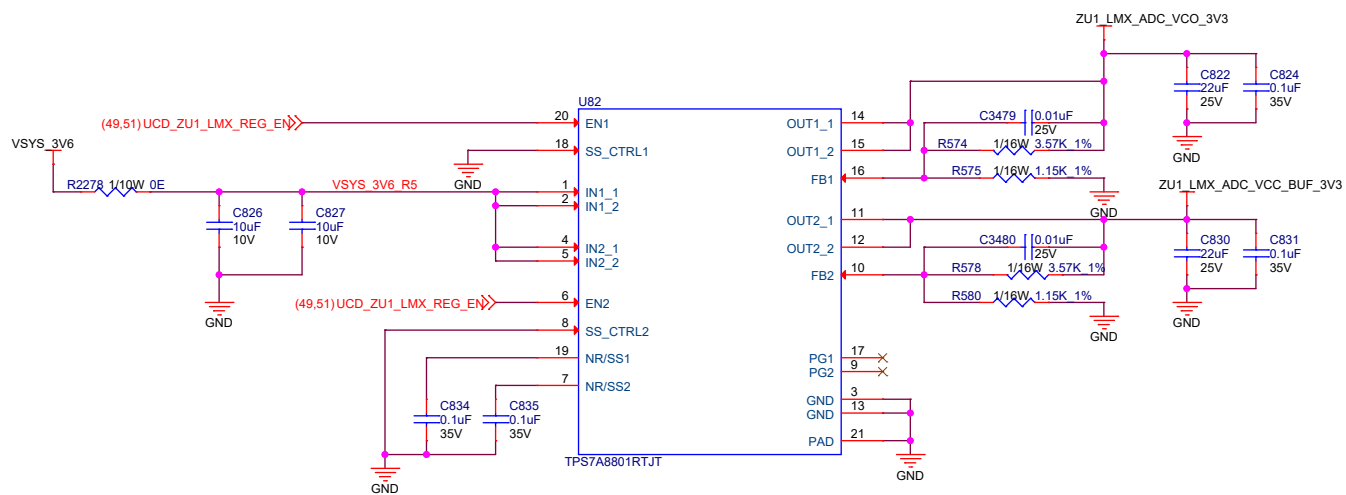


5

LMK LDOs

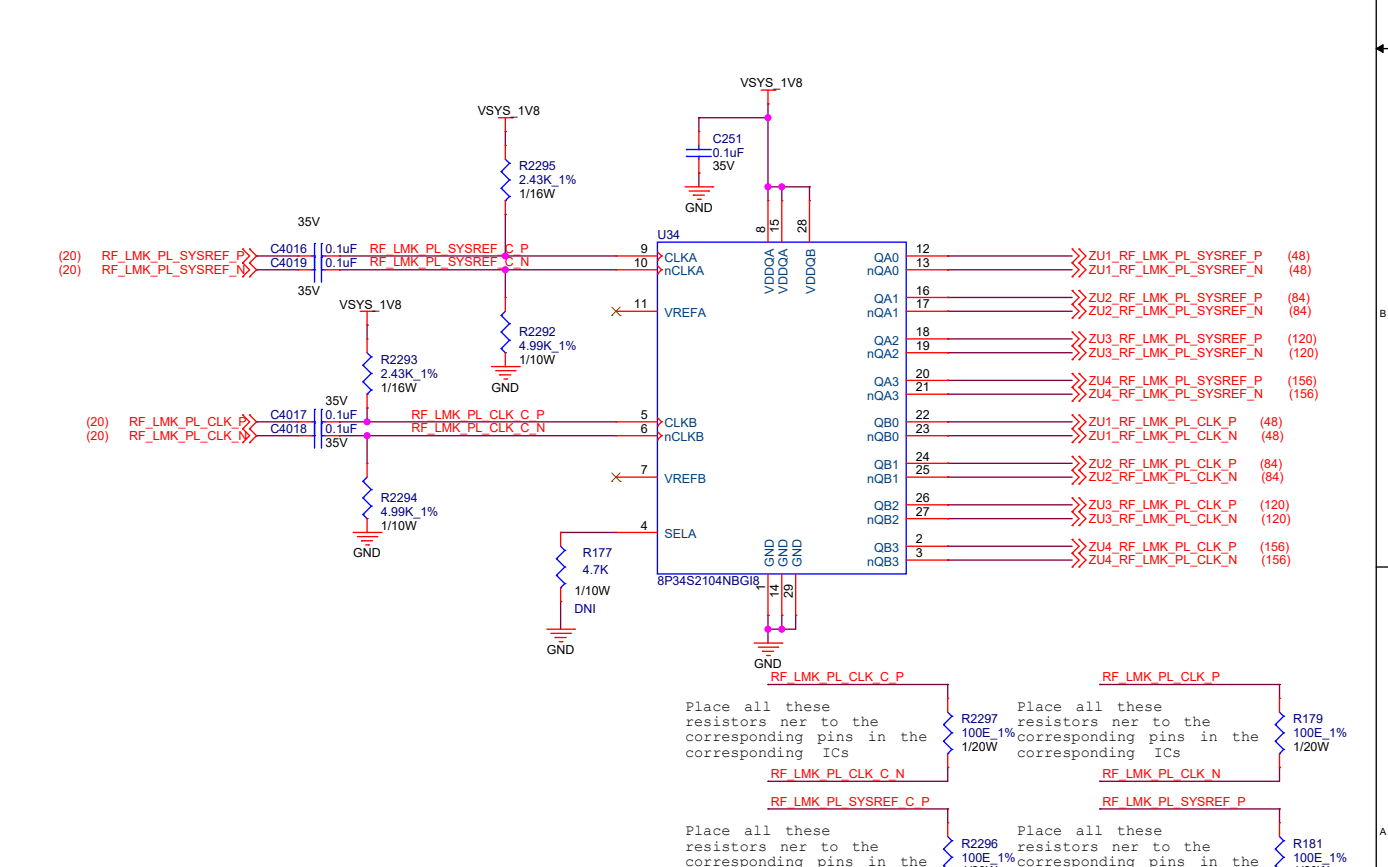
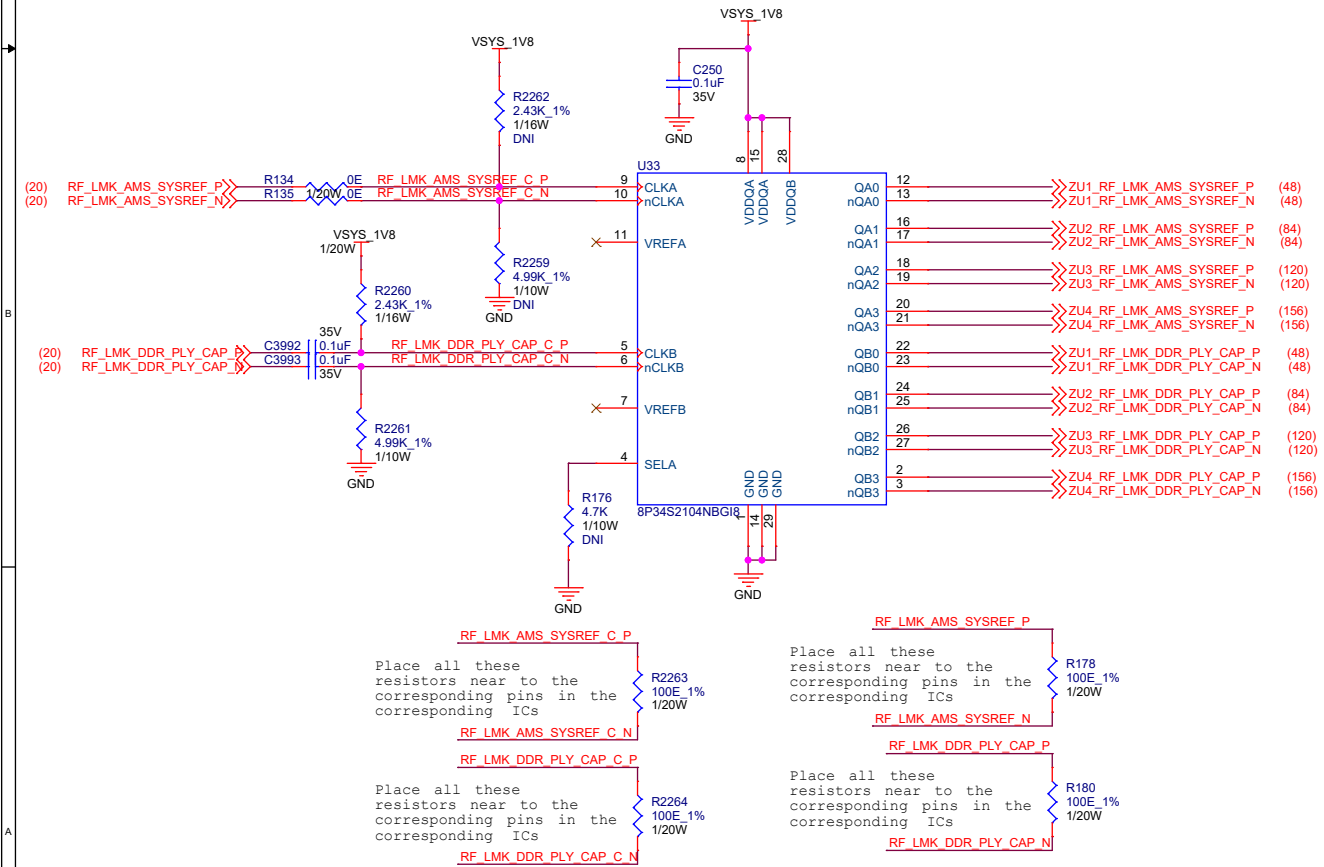
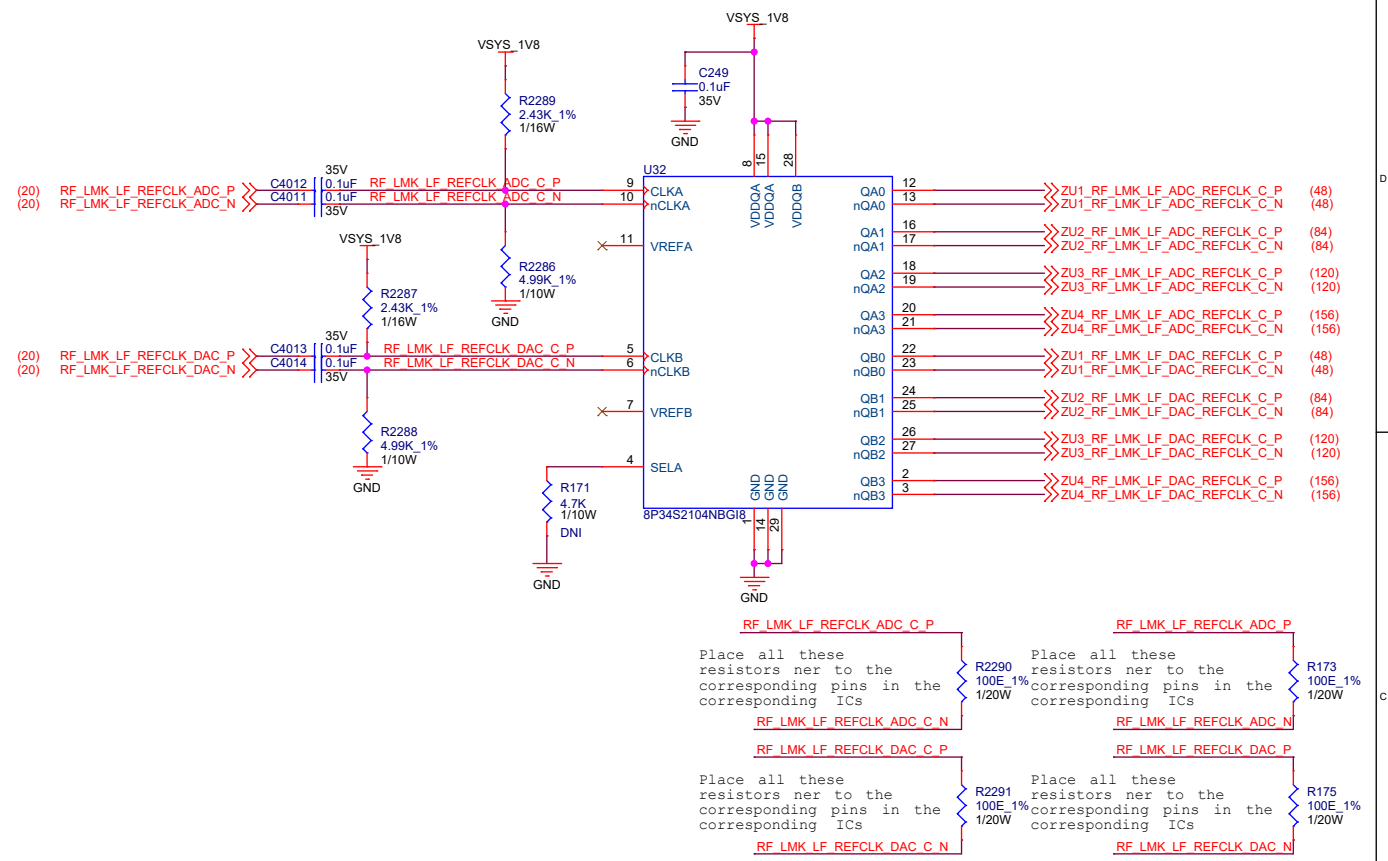
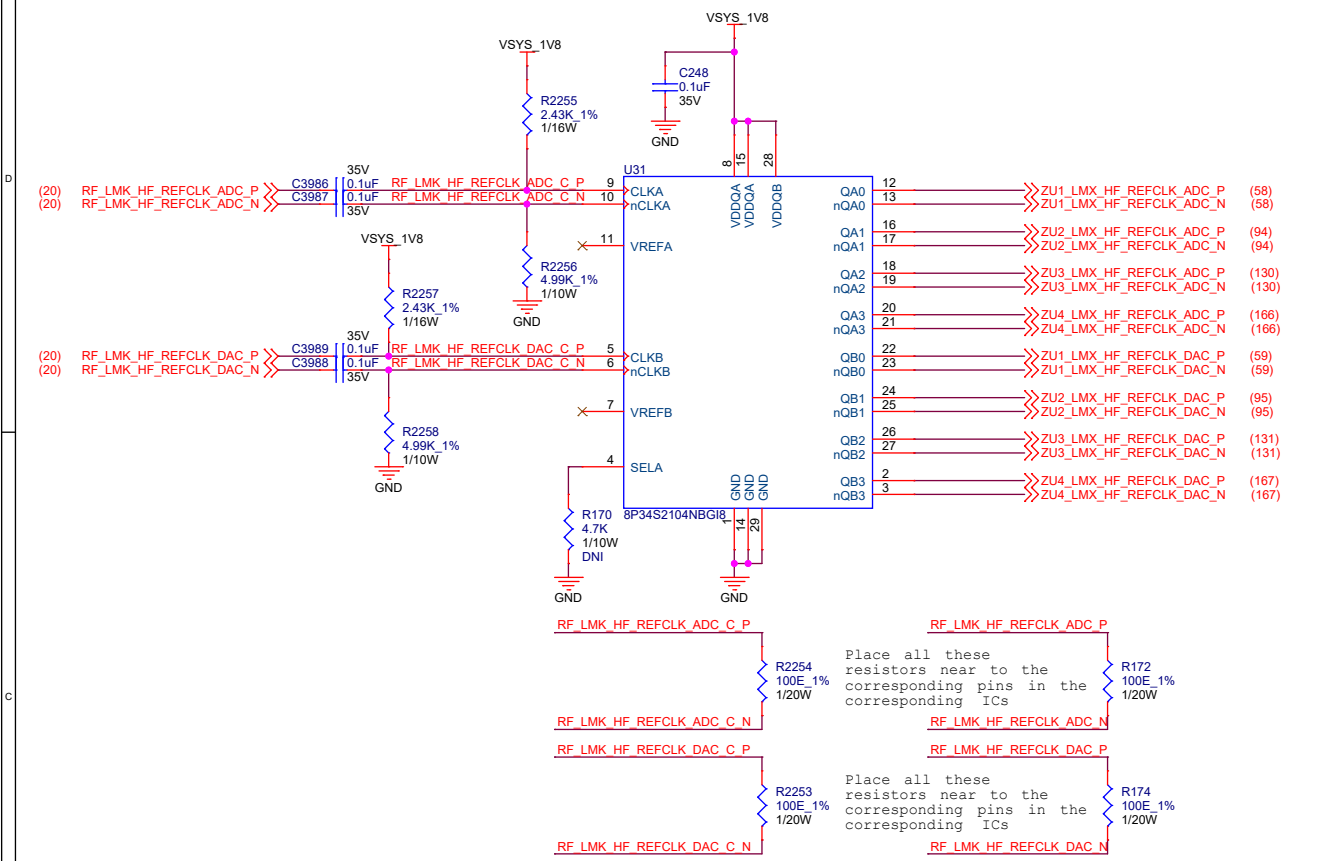


LMX ADC 3.3V LDO's



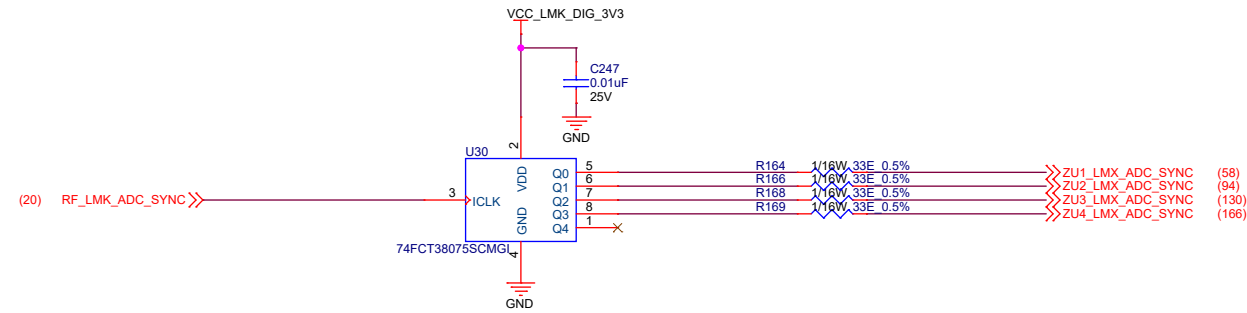
Title			
<Title>			
Size	Document Number		Rev
C	<Doc>		<RevCode>
Date:	Monday, January 11, 2021		Sheet 1 of 1

LMK CLOCK OUTPUT BUFFERS

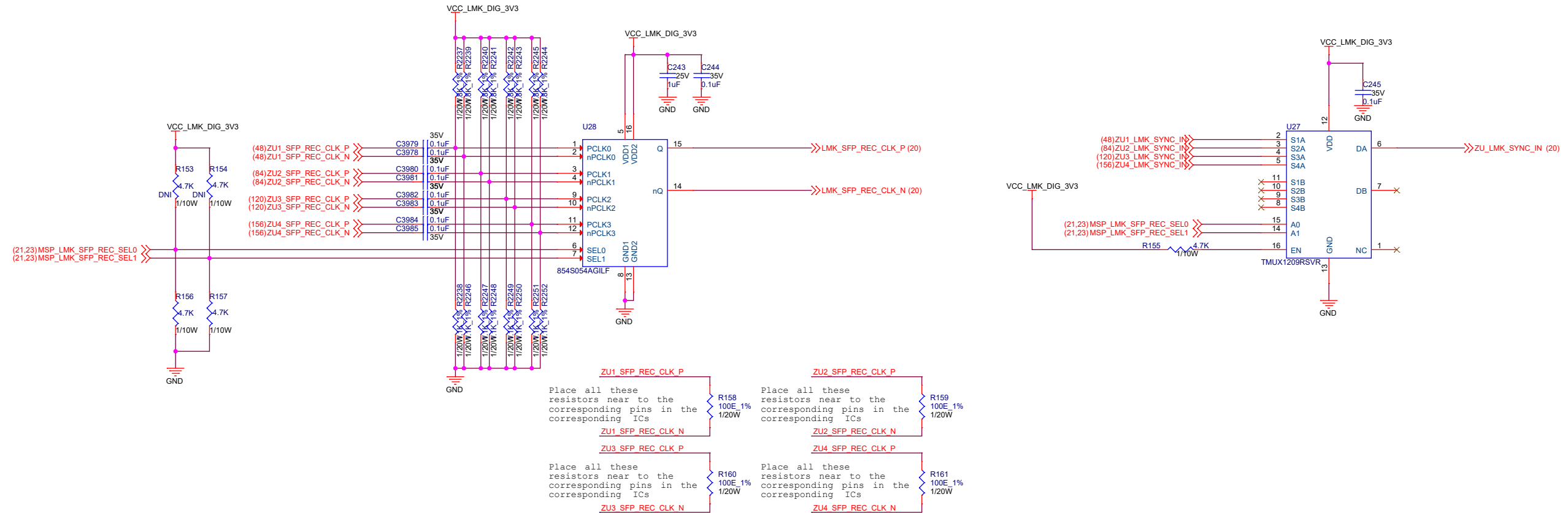


Title			
<Title>			
Size	Document Number		Rev
C	<Doc>		<RevCode>
Date:	Wednesday, January 13, 2021		Sheet 1 of 1

ADC LMX INPUT SYNC BUFFERS



LMK INPUT SFP REC CLK & SYNC BUFFERS



Title <Title>			
Size C	Document Number <Doc>		Rev <RevCode>
Date:	Wednesday, January 13, 2021		Sheet 1 of 1